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Docket No.: 0879-0277P

jc825 U.S. PTO
09/662323
09/14/00

Assistant Commissioner for Patents
Box PATENT APPLICATION
Washington, D.C. 20231

Sir:

Transmitted herewith for filing is the patent application of

Inventor(s): MATSUI, Seiichi

For: IMAGING APPARATUS, SOLID IMAGING DEVICE AND DRIVING METHOD
FOR SOLID IMAGING DEVICE

Enclosed are:

- X A specification consisting of 16 pages
- X 6 sheet(s) of Formal drawings
- X An assignment of the invention
- X Certified copy of Priority Document(s)
- X Executed Declaration X Original Photocopy
- A verified statement to establish small entity status under 37 CFR 1.9 and 37 CFR 1.27
- Preliminary Amendment
- Information Disclosure Statement, PTO-1449 and reference(s)

Other _____

The filing fee has been calculated as shown below:

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| TOTAL CLAIMS | 12 - 20 = | 0 | x18 =\$ 0.00 | or | x 9 = \$ 0.00 |
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| MULTIPLE DEPENDENT CLAIM PRESENTED <u>no</u> | | | +260 = \$ 0.00 | or | +130 = \$ 0.00 |
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Respectfully submitted,

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IMAGING APPARATUS, SOLID IMAGING DEVICE AND DRIVING METHOD FOR SOLID IMAGING DEVICE

BACKGROUND OF THE INVENTION

Field of the Invention

5 The present invention generally relates to an imaging apparatus that
images a subject with a solid imaging device or a CCD and stores image signals
with high definition in a recording device such as a semiconductor memory card,
the solid imaging device and the driving method for the solid imaging device.
The present invention relates more particularly to an imaging apparatus that
10 reduces data with high definition from a CCD in which pixel data of a plurality
of lines composes color information and transmits the data, an imaging method
and a solid imaging device.

Description of Related Art

15 The number of pixels of a CCD is sharply rising to improve dispersive
power of shot images in recent years. When the images are sequentially
displayed on a displaying device, it takes a long time to process the image data
from the CCD and the frame rate (updating period of displayed images) is low.
To address this problem, Japanese Patent Provisional Publication No. 7-312714
discloses an electronic camera that reduces image signals with high definition
20 acquired from a high-definition-CCD with color filter arrangement of G stripe
and R and B complete check or R, G and B stripe to produce image signals with
low definition and displays an image.

 The electronic camera disclosed in Japanese Patent Provisional
Publication No. 7-312714 is effective for the high-definition-CCD with color

filter arrangement (G stripe and R and B complete check or R, G and B stripe) in which image data of one line composes color information. However, in case of a CCD with Bayer arrangement in which image data of two lines composes color information, the image easily has untrue colors when intervals at reducing of the image data are long, and thus the quality of the image displayed on a monitor is poor.

An example of a method of reducing data outputted from a CCD with Bayer arrangement is shown in Fig. 6, which is proposed in pending U.S. Patent Application Nos. 09/492,093 and 09/570,733 assigned to the assignee of the present application. In this example, when data outputted from a CCD 112 with Bayer-arranged photoelectrically transferring devices 140 is to be reduced, image data of the first data is obtained from pixel data of the first line and pixel data of the second line and pixel data of the third line through the eighth line is eliminated and image data of the second data is obtained from the pixel data of the second line and pixel data of the ninth line and image data of the third line is obtained from the pixel data of the ninth line and pixel data of the tenth line. In this case, the image data of the first line and the image data of the third line are obtained from the pixel data of the adjoining lines, and thus right image data can be obtained. However, the image data of the second line is obtained from the lines that are not adjoining to each other, and thus the image displayed on the monitor has untrue colors.

SUMMARY OF THE INVENTION

The present invention has been developed in view of the above-described circumstances, and has as its object the provision of an imaging apparatus with a high-definition solid imaging device that can maintain a frame rate high and acquire reduced image signals without untrue colors, an imaging method and the

solid imaging device.

To achieve the above-described object, the present invention is directed to a solid imaging device in which pixel information of two adjoining lines composes color information of three primary colors, said solid imaging device
5 comprises transferring gates to which gate pulses for transferring only the pixel information of pairs of two adjoining lines with intervals of a plurality of lines to vertical transferring routes are applied when image signals with low definition are produced.

According to the present invention, the solid imaging device has the
10 transferring gates to which the gate pulses for transferring only the pixel information of the pairs of two adjoining lines with the intervals of a plurality of lines to the vertical transferring routes are applied when the image signals with the low definition are produced. Thus, the frame rate can be maintained high and the reduced image signals without untrue colors can be obtained.

To achieve the above-described object, the present invention is directed
15 to an imaging apparatus comprising: said solid imaging device; a shooting optical system that forms a subject image on a receiving surface of said solid imaging device; a timing generator that drives said solid imaging device and reads pixel information from said solid imaging device, the timing generator
20 applying gate pulses for transferring only pixel information of pairs of two adjoining lines with intervals of a plurality of lines to vertical transferring routes to said transferring gates when image signals with low definition are produced; and a signal processing device that produces the image signals by producing
25 pixel information of one line from the pixel information of each pair of two adjoining lines read from said solid imaging device when the image signals with the low definition are produced.

According to the present invention, the imaging apparatus has the solid imaging device; the shooting optical system that forms the subject image on the

receiving surface of the solid imaging device; the timing generator that drives the solid imaging device and reads the pixel information from the solid imaging device, the timing generator applying the gate pulses for transferring only the pixel information of the pairs of two adjoining lines with the intervals of a plurality of lines to the vertical transferring routes to the transferring gates when the image signals with the low definition are produced; and the signal processing device that produces the image signals by producing the pixel information of one line from the pixel information of each pair of two adjoining lines read from the solid imaging device when the image signals with the low definition are produced. Thus, the imaging apparatus can maintain the frame rate high and obtain the reduced image signals without untrue colors.

BRIEF DESCRIPTION OF THE DRAWINGS

The nature of this invention, as well as other objects and advantages thereof, will be explained in the following with reference to the accompanying drawings, in which like reference characters designate the same or similar parts throughout the figures and wherein:

Fig. 1 is a block diagram showing the structure of an imaging apparatus according to the present invention;

Fig. 2 is a diagram showing a method of obtaining image signals with high definition and an imaging method in case of a CCD with filter arrangement in which pixel data of two adjoining lines compose color information;

Fig. 3 is a diagram showing the outputting method in case the pixel data of the CCD with filter arrangement in which pixel data of two adjoining lines compose color information is reduced to 1/4 to be outputted as image signals with low definition;

Fig. 4 is a graph diagram showing a signal processing in which the data is

reduced to 1/4 and outputted as image signals with low definition to display the image on the monitor in case of the CCD with Bayer arrangement;

Fig. 5 is a diagram showing a signal processing and the interpolation in which the data is reduced to 1/4 and outputted as image signals with low definition to display the image on the monitor in case of the CCD with Bayer arrangement; and

Fig. 6 is a diagram showing a comparative example of a method of reducing data outputted from a CCD.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

Hereunder the preferred embodiment of the present invention is explained in detail according to the accompanying drawings.

Fig. 1 is a block diagram showing the structure of an imaging apparatus according to the present invention.

As shown in Fig. 1, an electronic camera 10 that is an embodiment of the imaging apparatus comprises a lens 14 that forms a subject image on a CCD 12, a diaphragm 16 that adjusts an amount of light reaching the CCD 14, a lens driving part 18 that adjusts the position where the subject image is formed on the CCD 12, a system controlling part 20 that performs control of the whole electronic camera 10, for example, control of the lens driving part 18 and processing of imaging signals, the CCD 12 and a timing generator 22 that transmits timing signals controlling a reduction rate, a frame rate and a pixel number of image data to synchronize the CCD 12 and the system controlling part 20. The electronic camera 10 also comprises a signal processing part 24 that performs sharpness correction, gamma correction, contrast correction, white balance correction, YC conversion, compression and so on of image data, a recording device 26 that records the shot image, a displaying and outputting

device 28 that displays the processed image data or outputs the image data to the outside and a memory 30 that temporarily stores the image data.

The operation of the electronic camera 10 constructed as described above will now be explained.

5 When the user sets the electronic camera 10 to a shooting mode, the subject image formed on the CCD 12 is reduced for a frame rate and a data amount suitable for display and transmitted to the displaying device through the signal processing device 24, and the displaying device displays the image.

10 Then, the user turns the electronic camera 10 to the subject, and presses a shutter release button (not shown) to perform a shooting after determining the camera angle, the focus and the exposure while looking at the image displayed by the displaying device.

The data of the image formed on the CCD 12 and displayed is recorded in the recording device 26 with a preset frame rate and pixel number.

15 The method of reducing the image data will now be explained.

Fig. 2 is a diagram showing a method of obtaining image signals with high definition and an imaging method in case of a CCD with filter arrangement in which pixel data of two adjoining lines compose color information.

20 As shown in Fig. 2, $(1+2n)$ -th lines ($n=0, 1, 2, 3, \dots$) of the CCD 12 have photoelectrically-transferring devices 40 with filter arrangement of R, G, R and G, and $(2+2n)$ -th lines have photoelectrically-transferring devices 40 with filter arrangement of G, B, G and B.

25 The photoelectrically-transferring devices 40 convert received lights into electric charges, and the accumulated electric charges are transferred to vertical transferring routes 44 through transferring gates 42 adjoining to the photoelectrically-transferring devices 40. The transferring gates 42 generally open and close on the line-by-line basis according to gate pulses TGn.

The electric charges acquired by the photoelectrically-transferring

devices 40 are sequentially transferred downward in Fig. 2 according to vertically-transferring pulses $\phi V1-\phi V4$. One end of each vertical transferring route 44 is connected to a horizontal transferring route 46, and the electric charges transferred from the vertical transferring routes 44 are sequentially transferred to the left in Fig. 2 according to horizontally-transferring pulses $\phi H1-\phi H4$ and outputted from the CCD 12.

Generally, the same signal line is used for the gate pulses TGn and the vertically-transferring pulses $\phi V1-\phi V4$. For example, the CCD 12 is constructed so that the transferring gates 42 open when a positive voltage is applied to the signal line and the transferring gates 42 close when a zero or negative voltage is applied to the signal line. Also, the CCD 12 is constructed so that gates of the vertical transferring routes 44 are controlled to transfer the photoelectrically-transferred electronic charges when a negative voltage is applied to the signal line. Thus, the number of the control lines of the CCD 12 can be reduced to the minimum and the CCD 12 can be small and easy to control.

The operation of the CCD 12 constructed as described above will now be explained.

When the transferring gates 42 are closed, the electric charges corresponding to the amounts of the received lights are accumulated in the photoelectrically-transferring devices 40. When the electric charges are to be outputted from the photoelectrically-transferring devices 40, the transferring gates 42 adjoining to the photoelectrically-transferring devices 40 are opened when the gate pulses TGn are given, and the accumulated electric charges are outputted to the vertical transferring routes 44. In Fig. 2, the gate pulses $TG1-TG16$ are given at one time to output the data of all the lines to the vertical transferring routes 44 at one time. If the accumulated electric charges are divided into data of two fields, data of alternate lines is outputted to the vertical

transferring routes 44.

The electric charges transferred to the vertical transferring routes 44 are sequentially transferred downward in Fig. 2 according to the vertically-transferring pulses $\phi V1$ - $\phi V4$. The electric charges transferred from the vertical transferring routes 44 are sequentially transferred to the left in Fig. 2 on the line-by-line basis according to the horizontally-transferring pulses $\phi H1$ - $\phi H4$ and outputted from the CCD 12. If the data of all the lines are outputted from the CCD 12, data of the sixteenth line, the fifteenth line, ..., the second line, the first line is outputted in that order.

If the electric charges are divided into data of two fields, data of the sixteenth line, the fourteenth line, the twelfth line, ..., the second line is sequentially outputted in that order as the data of the first field, and then data of the fifteenth line, the thirteenth line, the eleventh line, ..., the first line is sequentially outputted in that order as the data of the second field. The acquired image data of the fields is temporarily stored in the memory 30. The signal processing device 24 reads the image data stored in the memory 30 and converts the image data into R, G and B signals by combining the image data of the fields.

Fig. 3 is a diagram showing the outputting method in case the pixel data of the CCD with filter arrangement in which pixel data of two adjoining lines compose color information is reduced to 1/4 to be outputted as image signals with low definition.

In Fig. 3, the gate pulses TG1, TG2, TG9 and TG10 are given at one time so that the data of the third-eighth lines and the eleventh-sixteenth lines is eliminated.

The electric charges of the first, the second, the ninth and the tenth lines transferred to the vertical transferring routes 44 are sequentially transferred downward in Fig. 2 according to the vertically-transferring pulses $\phi V1$ - $\phi V4$.

The electric charges transferred from the vertical transferring routes 44 are sequentially transferred to the left in Fig. 2 according to the horizontally-transferring pulses $\phi H1-\phi H4$ and outputted from the CCD 12. The data of the tenth line, the ninth line, the second line and the first line is sequentially outputted from the CCD 12 in that order.

To eliminate the electric charges of the third-eighth lines and the eleventh-sixteenth lines, a drain (not shown) may be connected to the vertical transferring routes 44 on the opposite side from the horizontal transferring routes 46, and over flow drains similar to the transferring gates 42 are provided to the photoelectrically-transferring devices 40.

The system controlling part 20 outputs instructions for controlling the gate pulses TGn to the timing generator 22, and the reading method is selected according to the definition of the displaying device and the mode. For example, the data of all the pixels is read when the image is to be recorded in the recording device 26, and the data of the pixels is reduced to 1/2 to be read when the image is to be displayed on only the TV monitor, and the data of the pixels is reduced to 1/4 to be read when the image is to be displayed on only the LCD monitor. The signal processing device 24 is controlled by the system controlling part 20, and a signal processing method is selected according to the mode.

The gate pulses TGn are controlled on the line-by-line basis and the data of the horizontal lines are eliminated in the above explanation, but data of vertical pixels may be eliminated, and the signal processing device 24 may reduce the electric charges outputted from the horizontal transferring routes 46.

Fig. 4 shows the signal processing in which the data is reduced to 1/4 and outputted as image signals with low definition to display the image on the monitor in case of the CCD with Bayer arrangement.

R, G and B data of the first line is produced from the R, G, R, G, R and G data of the first line and the G, B, G, B, G and B data of the second line. R, G

and B data of the second line is produced not from the G, B, G, B, G and B data of the second line and the R, G, R, G, R and G data of the ninth line but from the R, G, R, G, R and G data of the ninth line and the G, B, G, B, G and B data of the tenth line. The R, G and B data for monitor display is sequentially produced from only data of the adjoining lines in this way, and thus the clear image without untrue colors can be displayed on the monitor even if some lines are eliminated.

In the embodiment, the CCD has Bayer arrangement and six consecutive lines are eliminated, but the present invention is not limited to this and the number of the eliminated lines can be changed according to the structure of the system.

Fig. 5 shows the signal processing and the interpolation in which the data is reduced to 1/4 and outputted as image signals with low definition to display the image on the monitor in case of the CCD with Bayer arrangement.

As shown in Fig. 5, the data is reduced in the same way as in Fig. 4 and the signal processing device 24 interpolates the reduced data with average values and so on to display the image on the monitor. The reduced data is interpolated to display the image on the monitor in this way, and thus the dispersive power can be improved while the frame rate can be maintained high.

As explained above, the solid imaging device according to the present invention has the transferring gates to which the gate pulses for transferring only the pixel information of the pairs of two adjoining lines with the intervals of a plurality of lines to the vertical transferring routes are applied when the image signals with the low definition are produced. Thus, the frame rate can be maintained high and the reduced image signals without untrue colors can be obtained.

As explained above, the imaging apparatus according to the present invention has the solid imaging device; the shooting optical system that forms

the subject image on the receiving surface of the solid imaging device; the timing generator that drives the solid imaging device and reads the pixel information from the solid imaging device, the timing generator applying the gate pulses for transferring only the pixel information of the pairs of two adjoining lines with the intervals of a plurality of lines to the vertical transferring routes to the transferring gates when the image signals with the low definition are produced; and the signal processing device that produces the image signals by producing the pixel information of one line from the pixel information of each pair of two adjoining lines read from the solid imaging device when the image signals with the low definition are produced. Thus, the imaging apparatus can maintain the frame rate high and obtain the reduced image signals without untrue colors.

It should be understood, however, that there is no intention to limit the invention to the specific forms disclosed, but on the contrary, the invention is to cover all modifications, alternate constructions and equivalents falling within the spirit and scope of the invention as expressed in the appended claims.

WHAT IS CLAIMED IS:

1. A solid imaging device in which pixel information of two adjoining lines composes color information of three primary colors, said solid imaging device comprises transferring gates to which gate pulses for transferring only the pixel information of pairs of two adjoining lines with intervals of a plurality of lines to vertical transferring routes are applied when image signals with low definition are produced.
2. The solid imaging device as set forth in claim 1, wherein said solid imaging device transfers pixel information of all vertical lines to said vertical transferring routes without reducing the pixel information when image signals with high definition are produced.
3. The solid imaging device as set forth in claim 1, wherein said solid imaging device divides pixel information of all vertical lines into pixel information of a plurality of fields to transfer the pixel information to said vertical transferring routes when image signals with high definition are produced.
4. An imaging apparatus comprising:
 - said solid imaging device as set forth in claim 1;
 - a shooting optical system that forms a subject image on a receiving surface of said solid imaging device;
 - a timing generator that drives said solid imaging device and reads pixel information from said solid imaging device, the timing generator applying gate pulses for transferring only pixel information of pairs of two adjoining lines with intervals of a plurality of lines to vertical transferring routes to said transferring

gates when image signals with low definition are produced; and

a signal processing device that produces the image signals by producing pixel information of one line from the pixel information of each pair of two adjoining lines read from said solid imaging device when the image signals with the low definition are produced.

5. The imaging apparatus as set forth in claim 4, wherein said timing generator applies gate pulses for transferring pixel information of all the vertical lines to said vertical transferring routes without reducing the pixel information to said transferring gates at least when image signals with high definition are produced.

6. The imaging apparatus as set forth in claim 4, wherein said timing generator applies gate pulses for dividing pixel information of all the vertical lines into pixel information of a plurality of fields to transfer the pixel information to said vertical transferring routes to said transferring gates at least when image signals with high definition are produced.

7. The imaging apparatus as set forth in claim 4, wherein said signal processing device reduces pixel information of horizontal lines when image signals with low definition are produced.

8. The imaging apparatus as set forth in claim 4, wherein:

said signal processing device has an interpolation operation device that interpolates the image signals with the low definition to produce image signals; and

said signal processing device outputs image signals including the produced image signals.

9. The imaging apparatus as set forth in claim 4, further comprising a displaying device that displays a shot image according to the image signals with the low definition.

10. The imaging apparatus as set forth in claim 4, further comprising a recording device that records the image signals of the high definition.

11. A driving method for a solid imaging device in which pixel information of two adjoining lines composes color information of three primary colors, comprising the steps of;

applying gate pulses for transferring pixel information of all the vertical lines to said vertical transferring routes without reducing the pixel information to said transferring gates when image signals with high definition are produced;

applying gate pulses for transferring only pixel information of pairs of two adjoining lines with intervals of a plurality of lines to said vertical transferring routes to said transferring gates when image signals with low definition are produced; and

producing pixel information of one line from the pixel information of each pair of two adjoining lines read from said solid imaging device when the image signals with the low definition are produced.

12. A driving method for a solid imaging device in which pixel information of two adjoining lines composes color information of three primary colors, comprising the steps of;

applying gate pulses for dividing pixel information of all the vertical lines into pixel information of a plurality of fields and transferring the pixel information to said vertical transferring routes to said transferring gates when image signals with high definition are produced;

applying gate pulses for transferring only pixel information of pairs of two adjoining lines with intervals of a plurality of lines to said vertical transferring routes to said transferring gates when image signals with low definition are produced; and

producing pixel information of one line from the pixel information of each pair of two adjoining lines read from said solid imaging device when the image signals with the low definition are produced.

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ABSTRACT OF THE DISCLOSURE

A CCD has transferring gates to which gate pulses for transferring only pixel information of pairs of two adjoining lines with intervals of a plurality of lines to vertical transferring routes are applied when the image signals with the low definition are produced. Thus, a frame rate can be maintained high and reduced image signals without untrue colors can be obtained.

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FIG. 1

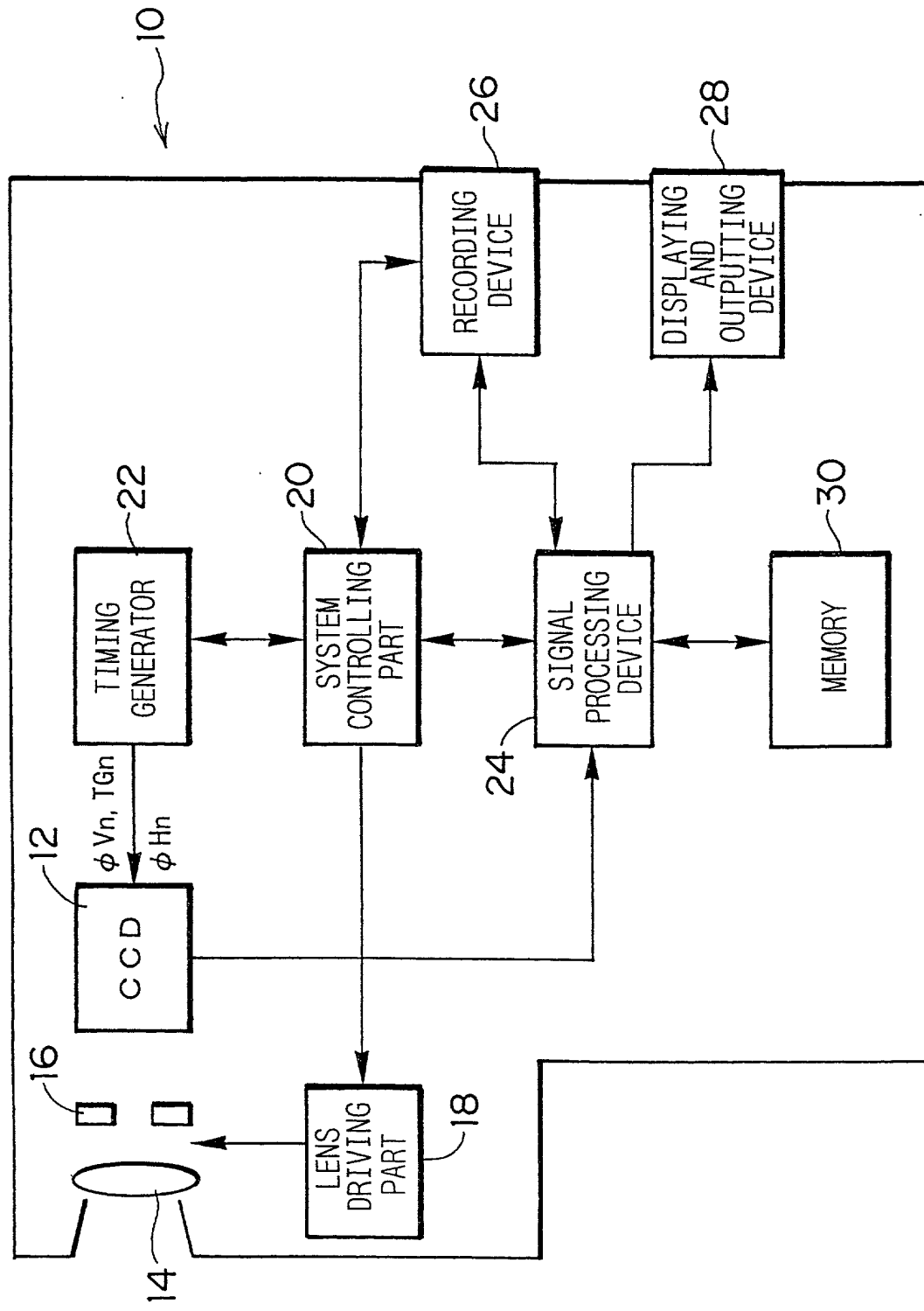


FIG. 2

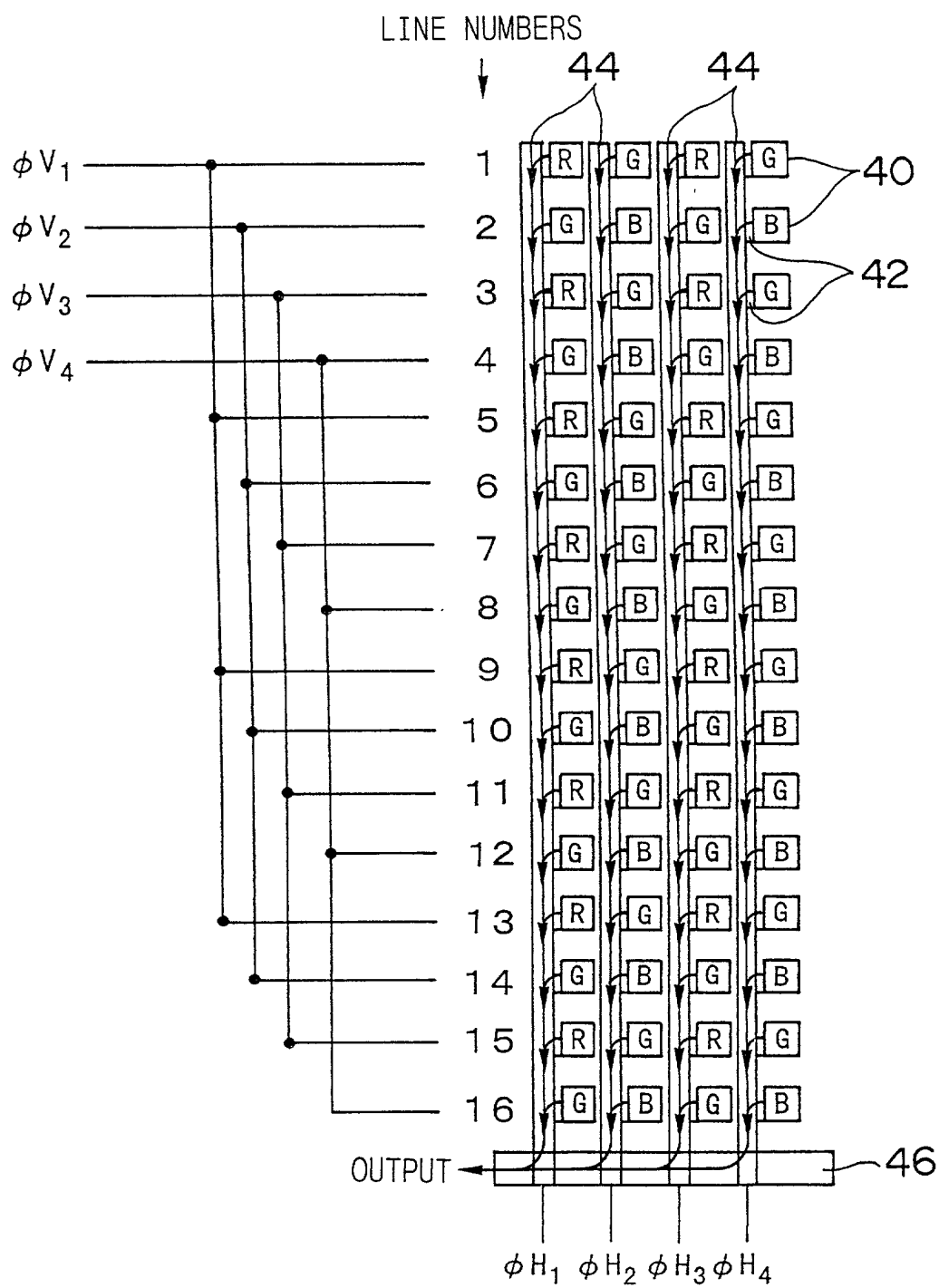


FIG. 3

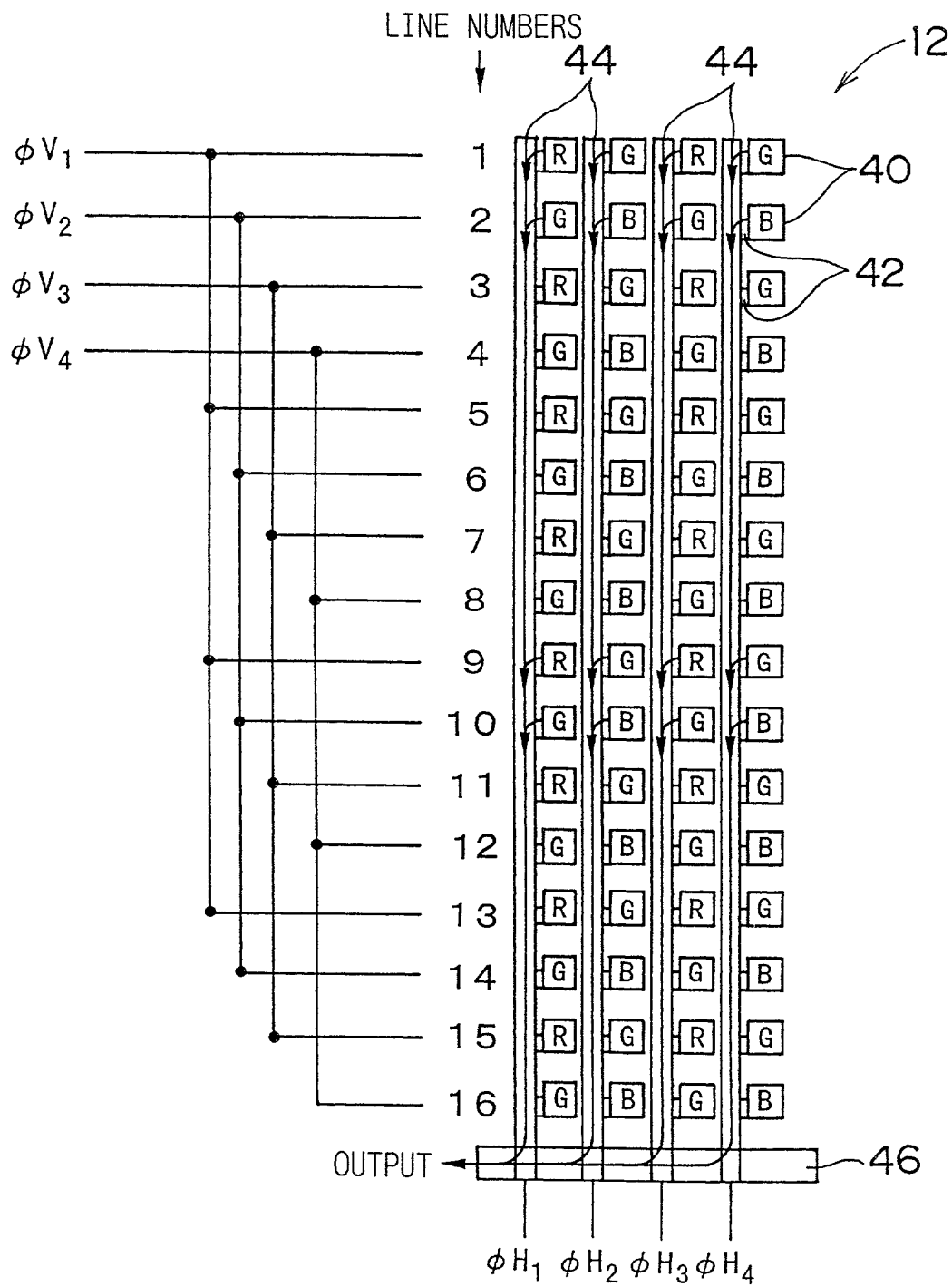


FIG. 4

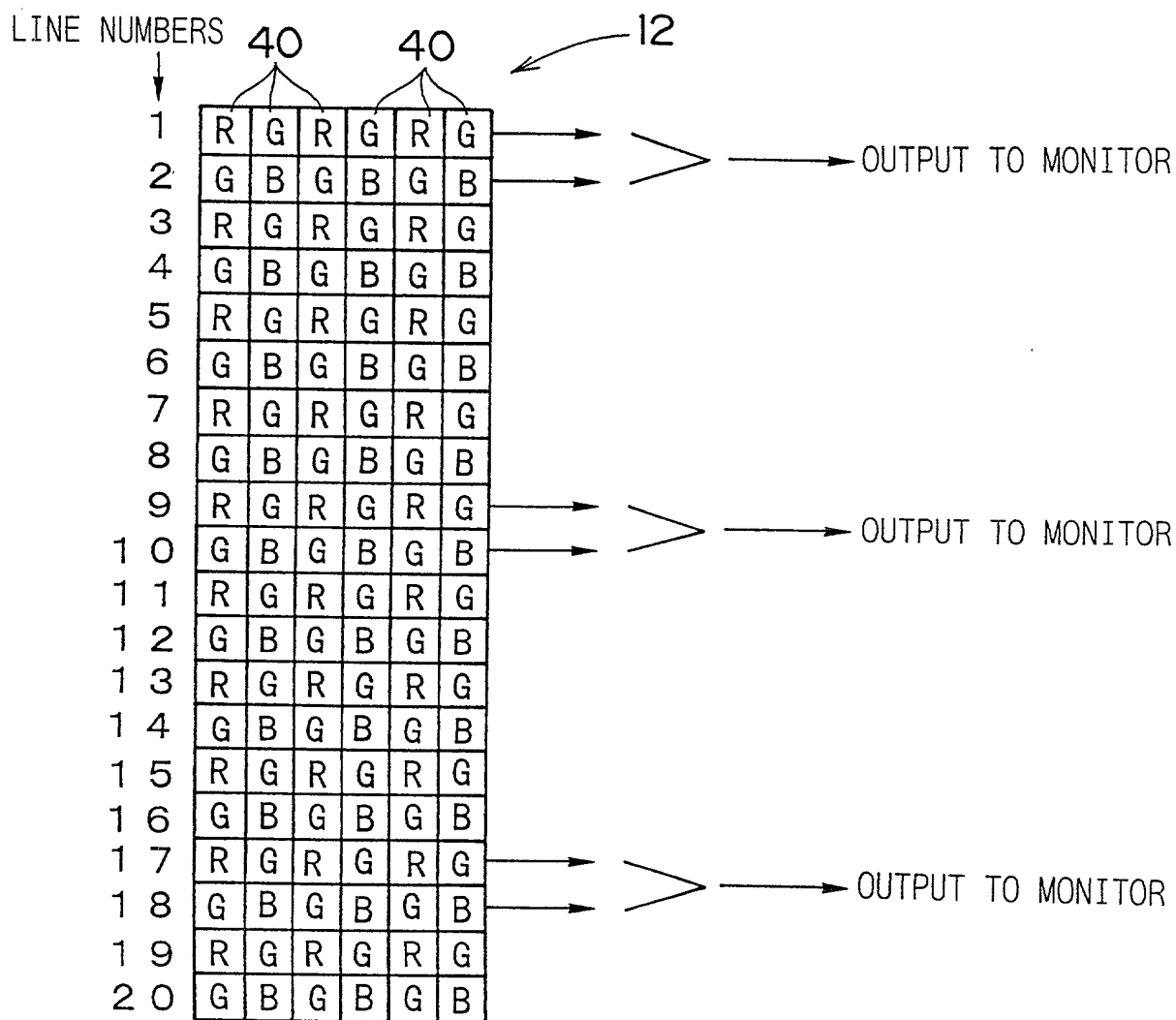


FIG. 5

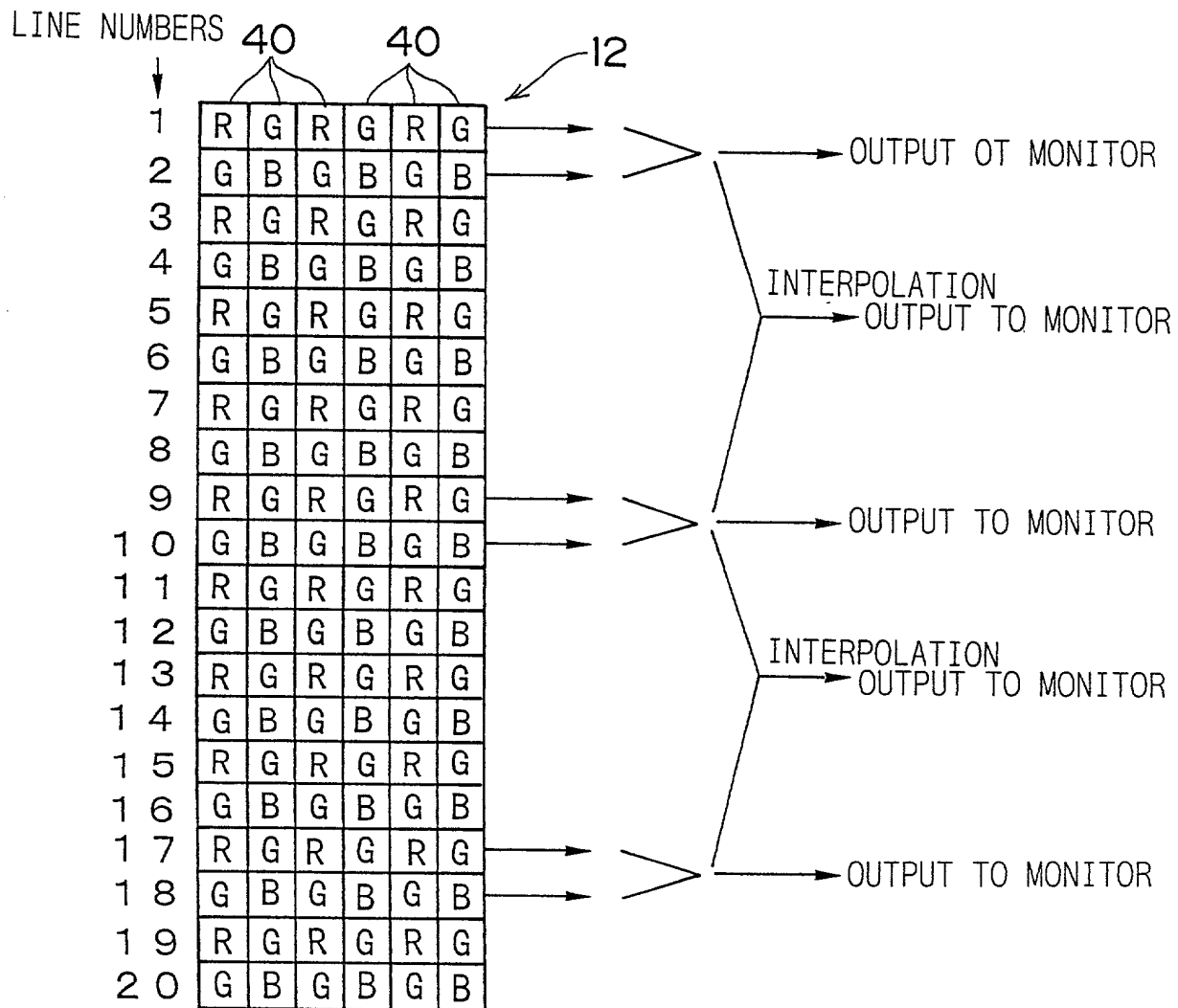
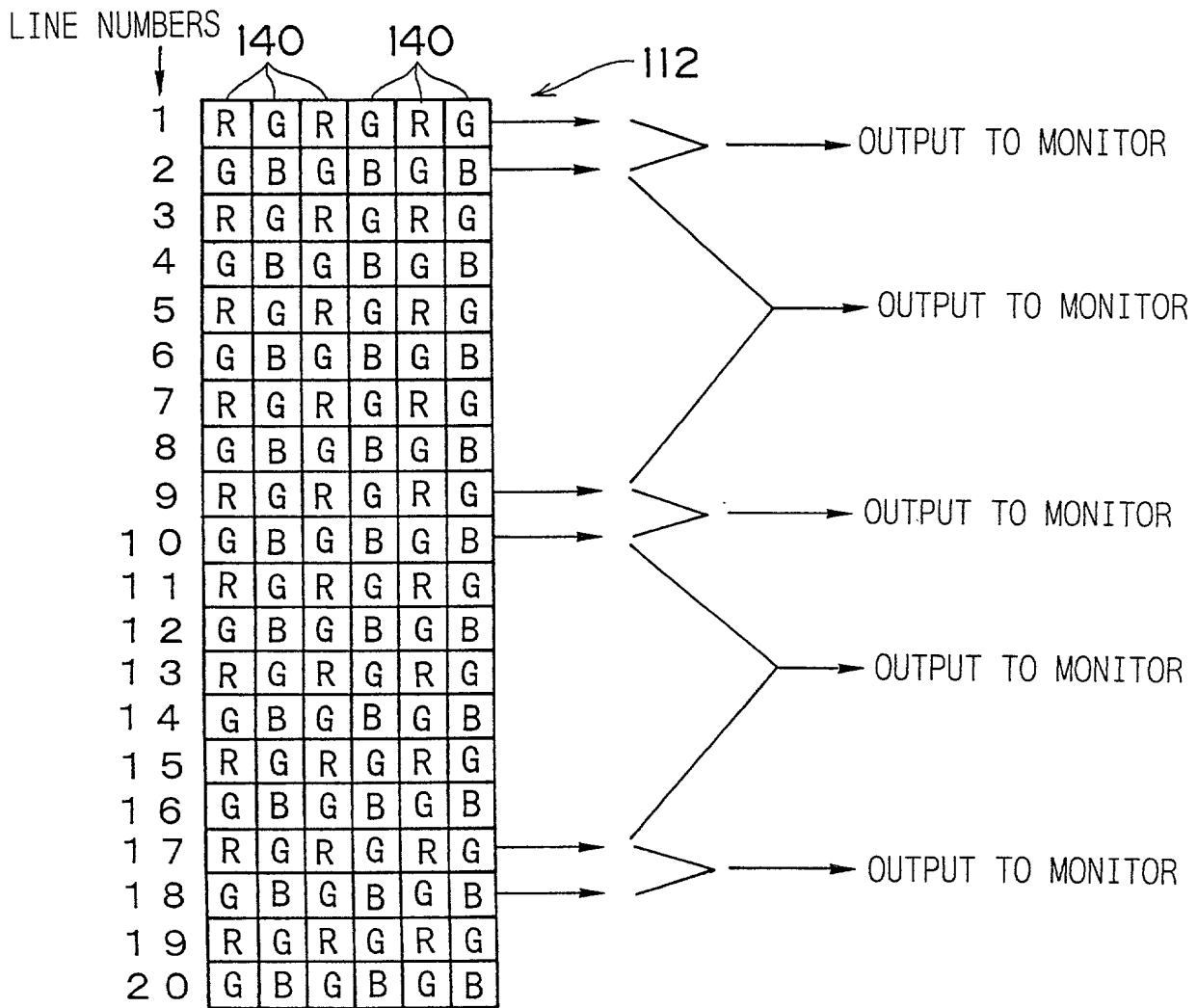


FIG. 6



PLEASE NOTE:
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Attorney Docket No.

0879-0277P

COMBINED DECLARATION AND POWER OF ATTORNEY FOR PATENT AND DESIGN APPLICATIONS

As a below named inventor, I hereby declare that: my residence, post office address and citizenship are as stated next to my name; that I verily believe that I am the original, first and sole inventor (if only one inventor is named below) or an original, first and joint inventor (if plural inventors are named below) of the subject matter which is claimed and for which a patent is sought on the invention entitled: **IMAGING APPARATUS, SOLID IMAGING DEVICE AND DRIVING**

METHOD FOR SOLID IMAGING DEVICE

Insert Title:

Fill in Appropriate
Information -
For Use Without
Specification
Attached:

the specification of which is attached hereto. If not attached hereto,

the specification was filed on _____ as
United States Application Number _____;
and amended on _____ (if applicable) and/or
the specification was filed on _____ as PCT
International Application Number _____; and was
amended under PCT Article 19 on _____ (if applicable)

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, §1.56.

I do not know and do not believe the same was ever known or used in the United States of America before my or our invention thereof, or patented or described in any printed publication in any country before my or our invention thereof or more than one year prior to this application, that the same was not in public use or on sale in the United States of America more than one year prior to this application, that the invention has not been patented or made the subject of an inventor's certificate issued before the date of this application in any country foreign to the United States of America on an application filed by me or my legal representative or assigns more than twelve months (six months for designs) prior to this application, and that no application for patent or inventor's certificate on this invention has been filed in any country foreign to the United States of America prior to this application by me or my legal representatives or assigns, except as follows.

I hereby claim foreign priority benefits under Title 35, United States Code, §119(a)-(d) of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s)

Priority Claimed

Insert Priority
Information:
(if appropriate)

| | | | | |
|---------------|-----------|------------------------|---|-----------------------------|
| No. 11-260519 | Japan | September 14, 1999 | <input checked="" type="checkbox"/> Yes | <input type="checkbox"/> No |
| (Number) | (Country) | (Month/Day/Year Filed) | | |
| _____ | _____ | _____ | <input type="checkbox"/> Yes | <input type="checkbox"/> No |
| (Number) | (Country) | (Month/Day/Year Filed) | | |
| _____ | _____ | _____ | <input type="checkbox"/> Yes | <input type="checkbox"/> No |
| (Number) | (Country) | (Month/Day/Year Filed) | | |
| _____ | _____ | _____ | <input type="checkbox"/> Yes | <input type="checkbox"/> No |
| (Number) | (Country) | (Month/Day/Year Filed) | | |

I hereby claim the benefit under Title 35, United States Code, §119(e) of any United States provisional applications(s) listed below.

Insert Provisional
Application(s):
(if any)

| | |
|----------------------|---------------|
| _____ | _____ |
| (Application Number) | (Filing Date) |
| _____ | _____ |
| (Application Number) | (Filing Date) |

All Foreign Applications, if any, for any Patent or Inventor's Certificate Filed More than 12 Months (6 Months for Designs) Prior to the Filing Date of This Application:

| Country | Application Number | Date of Filing (Month/Day/Year) |
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I hereby claim the benefit under Title 35, United States Code, §120 of any United States and/or PCT application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States and/or PCT application in the manner provided by the first paragraph of Title 35, United States Code, §112, I acknowledge the duty to disclose information which is material to the patentability as defined in Title 37, Code of Federal Regulations, §1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application.

Insert Prior U.S.
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| | | |
|----------------------|---------------|---|
| _____ | _____ | _____ |
| (Application Number) | (Filing Date) | (Status - patented, pending, abandoned) |
| _____ | _____ | _____ |
| (Application Number) | (Filing Date) | (Status - patented, pending, abandoned) |

I hereby appoint the following attorneys to prosecute this application and/or an international application based on this application and to transact all business in the Patent and Trademark Office connected therewith and in connection with the resulting patent based on instructions received from the entity who first sent the application papers to the attorneys identified below, unless the inventor(s) or assignee provides said attorneys with a written notice to the contrary:

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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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